





ELECTRONIC INFORMATION DISCLOSURE STATEMENT

Electronic Version v18

Stylesheet Version v18.0

Title of Invention	NON-VOLATILE TRANSISTOR MEMORY ARRAY INCORPORATING READ-ONLY ELEMENTS WITH SINGLE MASK SET						
Application Number: 10/810035 							
Confirmation Number: 8458							
First Named Applicant: Albert Weiner							
Attorney Docket Number: ATM-291							
Art Unit: 2818							
Search string: (6396767).pn.							
US Patent Documents							
Note: Applicant is not required to submit a paper copy of cited US Patent Documents							
init	Cite.No.	Patent No.	Date	Patentee	Kind	Class	Subclass
DM	1	6396767	2002-05-28	Denny Tzeng et al.	B1	365	230.06
Signature 							
Examiner Name				Date			
THINH T. NGUYEN				8/18/2005			